IEEE/IEE Publications Ondisc Jan 1990 - Sep 1996

Search Options:
    Search for both singular and plurals: YES
    Search for spelling variants   : NO
    Display intermediate result sets   : NO

Num	Search	Hits
#1	(separate or distinct or respective or different) w/5 (api or (application program? interface?))	3
#2	(independent or individual) w/5 (api or (application program? interface?))	12
#3	(independent or individual) w/3 (api or (application program? interface?))	10
#4	(set? or group? or family or block?) w/5 ((i o) or (input output) or peripheral?)	222
#5	(different or similar) w/5 ((i o) or (input output) or peripheral?) w/5 interface?	3
#6	#1 or #2	15
#7	#4 and #6	1
#8	(access? w/5 (similar or different or respective) w/5 type? w/5 device? w/5 interface?)	0
#9	(access? w/5 (similar or different or respective) w/10 type? w/10 device? w/10 interface?)	0
#10	(access? w/10 (similar or different or respective) w/10 type? w/10 device? w/10 interface?)	0
#11	(similar or different or respective) w/10 type? w/10 device? w/10 interface?	6
#12	access? w/5 family w/5 device? w/5 interface?	0
#13	access? w/5 (similar or different or respective) w/5 device? w/5 interface?	0
#14	(similar or different or respective) w/5 device? w/5 interface?	14
#15	(plurality or multiple) w/5 interfaces	178
#16	#4 and #15	0
#17	#4 and (respective w/5 interface?)	0

=> d his

(FILE 'USPAT' ENTERED AT 14:41:31 ON 18 NOV 96)

SET PAGE SCROLL

L1   11910 S (PLURALITY OR MULTIPLE) (5A) APPLICATION#

L2   56 S L1 (P) (PROGRAM? (3A) INTERFAC?)

L3   7770 S (CHOOS? OR SELECT? OR CHOICE) (5A) INTERFAC?

L4   17 S L2 AND L3

L5   23 S L1 (P) L3   .

L6   6304 S (GROUP# OR SET# OR BLOCK# OR FAMIL?) (5A)
SERVICE#

L7   2 S L5 AND L6

L8   74 S L1 AND L3 AND L6

L9   1 S L4 AND L6

L10   113 S 395/828,834,862/CCLS

L11   247 S 395/309/CCLS

L12   93 S 395/200.2/CCLS

L13   446 S L10 OR L11 OR L12

L14   726 S (DEDICAT? (5A) INTERFAC?)

L15   0 S L1 (P) L14

L16   32 S L1 AND L14

L17   23 S L13 AND L14

L18   2 S L13 AND L16

L19   0 S L6 (P) L14

L20   88 S L6 AND L14

L21   3 S L13 AND L20

L22   38 S L3 (P) L14

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L23          2 S  L1 AND L22

L24          4 S  L2 AND L14

L25          4 S  L2 AND L13

L26          3 S  (SIMILAR INTERFACE REQUIREMENTS)

L27         92 S  (DIFFERENT OR PLURALITY OR MULTIPLE) (5A) (API
OR (APPLICAT

L28          7 S  (PLURALITY OR MULTIPLE OR DIFFERENT) (5A)
(SEPARATE OR DIST


         FILE 'JPOABS' ENTERED AT 15:51:17 ON 18 NOV 96


L29        195 S  L1

L30          0 S  L2

L31        886 S  L3

L32       1376 S  L6

L33         35 S  L14

L34          0 S  L26

L35          1 S  L27

L36          0 S  L28

L37          0 S  L29 AND L31

L38          2 S  L31 AND L32

L39          0 S  L29 AND L33


=>
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=> d l26 1-3 ti

US PAT NO: 5,455,861 [IMAGE AVAILABLE]   L26: 1 of
3

TITLE: Secure telecommunications

US PAT NO: 5,434,920 [IMAGE AVAILABLE]   L26: 2 of
3

TITLE: Secure telecommunications

US PAT NO: 5,331,213 [IMAGE AVAILABLE]   L26: 3 of
3

TITLE: Pulse transmission circuit with undershoot
eliminating

                circuitry

=> d l25 1-4 ti

US PAT NO: 5,572,675 [IMAGE AVAILABLE]   L25: 1 of
4

TITLE: Application program interface

US PAT NO: 5,537,626 [IMAGE AVAILABLE]   L25: 2 of
4

TITLE: Apparatus for coupling printer with LAN to control
printer

                operation by transferring control parameters,
printer status

                data and printer configuration data between
printer and LAN

US PAT NO: 5,535,416 [IMAGE AVAILABLE]   L25: 3 of

TITLE: Method for allowing application program in computer system to

                access device directly in exclusive mode by bypassing

                operating system and blocking requests from other programs

US PAT NO: 5,513,365 [IMAGE AVAILABLE]   L25: 4 of 4

TITLE: Display adapter interface layer

=> d l24 1-4 ti

US PAT NO: 5,504,906 [IMAGE AVAILABLE]   L24: 1 of 4

TITLE: Managing structured objects using global definitions

US PAT NO: 5,497,463 [IMAGE AVAILABLE]   L24: 2 of 4

TITLE: Ally mechanism for interconnecting non-distributed computing

                environment (DCE) and DCE systems to operate in a network

                system

US PAT NO: 5,111,423 [IMAGE AVAILABLE]   L24: 3 of 4

TITLE: Programmable interface for computer system peripheral circuit

                card

US PAT NO: 4,379,326 [IMAGE AVAILABLE]   L24: 4 of
4

TITLE: Modular system controller for a transition machine

=> d l23 1-2 ti

US PAT NO: 5,111,423 [IMAGE AVAILABLE]   L23: 1 of
2

TITLE: Programmable interface for computer system
peripheral circuit

                card

US PAT NO: 4,982,325 [IMAGE AVAILABLE]   L23: 2 of
2

TITLE: Applications processor module for interfacing to a
database

                system

=> d l22 1-38 ti

US PAT NO: 5,568,134 [IMAGE AVAILABLE]   L22: 1 of
38

TITLE: Selective call receiver with computer interface
message

                notification

US PAT NO: 5,546,448 [IMAGE AVAILABLE]   L22: 2 of
38

TITLE: Apparatus and method for a caller ID modem
interface

US PAT NO: 5,546,442 [IMAGE AVAILABLE]   L22: 3 of

38

TITLE: Method and apparatus for use in completing
telephone calls

US PAT NO: 5,539,787 [IMAGE AVAILABLE]   L22: 4 of
38

TITLE: Converter for connecting modem equipment of
dissimilar

                interface protocol

US PAT NO: 5,512,886 [IMAGE AVAILABLE]   L22: 5 of
38

TITLE: Selective call receiver with computer interface

US PAT NO: 5,455,572 [IMAGE AVAILABLE]   L22: 6 of
38

TITLE: Selective call receiver with computer interface
message

                notification

US PAT NO: 5,404,545 [IMAGE AVAILABLE]   L22: 7 of
38

TITLE: Interface isolation and selection circuit for a
local area

                network

US PAT NO: 5,377,864 [IMAGE AVAILABLE]   L22: 8 of
38

TITLE: Drug dispensing apparatus

US PAT NO: 5,377,130 [IMAGE AVAILABLE]   L22: 9 of
38

TITLE: Method and apparatus for solving terms of a
numerical sequence

US PAT NO: 5,293,495 [IMAGE AVAILABLE]   L22: 10 of
38

TITLE: Method of addressing devices and transferring data
on a bus

US PAT NO: 5,264,958 [IMAGE AVAILABLE]   L22: 11 of
38

TITLE: Universal communications interface adaptable for a
plurality

                of interface standards

US PAT NO: 5,247,519 [IMAGE AVAILABLE]   L22: 12 of
38

TITLE: Selective call receiver programming system

US PAT NO: 5,111,423 [IMAGE AVAILABLE]   L22: 13 of
38

TITLE: Programmable interface for computer system
peripheral circuit

                card

US PAT NO: 5,043,938 [IMAGE AVAILABLE]   L22: 14 of
38

TITLE: Node controller for a local area network

US PAT NO: 5,041,963 [IMAGE AVAILABLE]   L22: 15 of
38

TITLE: Local area network with an active star topology
comprising

                ring controllers having ring monitor logic

function

US PAT NO: 5,032,783 [IMAGE AVAILABLE]   L22: 16 of
38

TITLE: Test circuit and scan tested logic device with
isolated data

                lines during testing

US PAT NO: 4,982,325 [IMAGE AVAILABLE]   L22: 17 of
38

TITLE: Applications processor module for interfacing to a
database

                system

US PAT NO: 4,937,785 [IMAGE AVAILABLE]   L22: 18 of
38

TITLE: Visual signal processing backplane bus

US PAT NO: 4,924,158 [IMAGE AVAILABLE]   L22: 19 of
38

TITLE: Motor driver protection circuit

US PAT NO: 4,792,896 [IMAGE AVAILABLE]   L22: 20 of
38

TITLE: Storage controller emulator providing transparent
resource

                sharing in a computer system

US PAT NO: 4,768,188 [IMAGE AVAILABLE]   L22: 21 of
38

TITLE: Optical demand assigned local loop communication
system

US PAT NO: 4,710,933 [IMAGE AVAILABLE]   L22: 22 of
38

TITLE: Parallel/serial scan system for testing logic
circuits

US PAT NO: 4,710,931 [IMAGE AVAILABLE]   L22: 23 of
38

TITLE: Partitioned scan-testing system

US PAT NO: 4,701,921 [IMAGE AVAILABLE]   L22: 24 of
38

TITLE: Modularized scan path for serially tested logic
circuit

US PAT NO: 4,698,588 [IMAGE AVAILABLE]   L22: 25 of
38

TITLE: Transparent shift register latch for isolating
peripheral

                ports during scan testing of a logic circuit

US PAT NO: 4,660,168 [IMAGE AVAILABLE]   L22: 26 of
38

TITLE: Apparatus for completing a customer initiated ATM
transaction

US PAT NO: 4,642,761 [IMAGE AVAILABLE]   L22: 27 of
38

TITLE: Dedicated I/O serial bit interface with
peripheral selection

US PAT NO: 4,636,947 [IMAGE AVAILABLE]   L22: 28 of
38

TITLE: ATM task scheduling system for simultaneous
peripheral device

                 transactions processing

US PAT NO: 4,621,326 [IMAGE AVAILABLE]   L22: 29 of
38

TITLE: Method of reducing customer transaction time in an
automatic

                 teller machine by parallel processing of
sequence events

US PAT NO: 4,593,352 [IMAGE AVAILABLE]   L22: 30 of
38

TITLE: Method and device for exchanging information
between terminals

                 and a central control unit

US PAT NO: 4,339,746 [IMAGE AVAILABLE]   L22: 31 of
38

TITLE: Alarm control center

US PAT NO: 4,288,733 [IMAGE AVAILABLE]   L22: 32 of
38

TITLE: Battery charger system and method adapted for use
in a

                 sterilized environment

US PAT NO: 4,268,722 [IMAGE AVAILABLE]   L22: 33 of
38

TITLE: Radiotelephone communications system

US PAT NO: 4,257,119 [IMAGE AVAILABLE]   L22: 34 of
38

TITLE: PCM switching system for wide and narrow band
signals

US PAT NO: 4,237,533 [IMAGE AVAILABLE]   L22: 35 of
38

TITLE: Preventing initial program load failures

US PAT NO: 4,160,131 [IMAGE AVAILABLE]   L22: 36 of
38

TITLE: Electronic key telephone system

US PAT NO: 4,148,073 [IMAGE AVAILABLE]   L22: 37 of
38

TITLE: High speed video display system including zoom
feature

US PAT NO: 4,120,003 [IMAGE AVAILABLE]   L22: 38 of
38

TITLE: Multiple channel CATV system utilizing row grabber
interface

         as CATV input